**LESSON PLAN -2023-24 (Odd Sem.)**

**Class: BCA 1st Sem. (Logical Organization of Computer)**

**Name:- Dr Rajesh Dawar Sub:- Logical Organization of Computer(BCA 104)**

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| **Month** | **1st Week** | **2nd Week** | **3rd Week** | **4th Week** |
| **JUL.** | -------- | -------------- | -- | Information Representaion-Number Systems |
| **AUG.** | Binary Arithmetic, Fixed and Floating point representation of numbers | BCD Codes,Arithmetic on BCD Codes | Error Detecting and correcting codes, Character representation Codes | Boolean Algebra- theorms of Boolean Algebra |
| **SEPT.** | Boolean functions and truth tables, SOP Form of Boolean Expression | POS form of Boolean Expression, | Simplification of Boolean Functions-K-Map and Venn Diagrams | Introduction to Digital Signals, Basic Gates |
| **OCT** | Universal Gates and their implementation, AND-OR-Invert and OR-AND-INVERT implementation | Combinational Logic Circuits-characteristics, Design procedure, analysis procedure, Multi Level NAND and NOR Circuits | Hald Adder, Full Adder Circuits, Half Subtractor | Full Subtractor, Parallel binary adder/subtractor, Encoders, Decoders |
| **NOV** | Multiplexers, Demultiplexers, comparators, code convertors, BCD to seven segment display | -- | Revision | -- |

**Teacher Signature**

**Dr. Rajesh Dawar**